

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 117863		APPLICATION NO. New U.S. Patent Application 10/721461	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANTS Eigo NAKAGAWA et al.			
				FILING DATE November 26, 2003		2829	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
R	1	JP A 2000-74998 w/ abst & trans	03/14/2000	Japan	—	—	
R	2	JP A 11-038111 w/ abst & trans	02/12/1999	Japan	—	—	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
	3	"Detection of Flaws in a Printed Circuit Board by Using an Eddy-Current Testing Approach" H. Fujiki et al; "http://www.magmac1.ec.t.kanazawa-u.ac.jp/magcap-j/research-j/ecta-j.html"					
EXAMINER		Timm Nguyen			DATE CONSIDERED 10/14/05		
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: November 26, 2003